

<b>Notice of References Cited</b>	Application/Control No. 09/976,920	Applicant(s)/Patent Under Reexamination MURPHY, CHARLES DOUGLAS	
	Examiner Chat C. Do	Art Unit 2193	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,434,586	08-2002	Carlson et al.	708/625
	B	US-6,141,674	10-2000	Unkrich et al.	708/628
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Naviner et al., Efficient implementation for high accuracy DCT processor based on FPGA, 1999, IEEE, page 508-511.
	V	Richard, Subexpression shring in filters using canonic signed digit multipliers, October 1996, IEEE Transactions on circuits and systems-II: analog and digital signal processing, Vol. 43, No. 10, pages 677-688.
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.